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Pardo, P. Lopez, D. Cabello. □. Departamento de Electronica y Computacion ...  
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RN Schneider, LE Turner, MM **Okoniewski** - Proceedings of the 2002 ACM/SIGDA tenth international ..., 2002 - sigda.org

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RN Schneider, MM **Okoniewski**, LE Turner - Microwave Symposium Digest, 2002 IEEE MTT-S International, 2002 - ieeexplore.ieee.org

... With one million cells, there are 10 logic gate equivalents available per three-dimensional **FDTD** cell, per **FPGA**. This is clearly not enough to ...

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**FDTD HARDWARE ACCELERATION SYSTEM**

M OKONIEWSKI, R SCHNEIDER, L TURNER - 2004 - freepatentsonline.com

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